

In the Claims

This listing of claims will replace all prior versions, and listings, of claims in the application:

1-2. (Cancelled)

3. (Currently Amended) ~~The~~ An optical output control circuit of ~~the~~ a semiconductor laser ~~according to claim 1, comprising:~~

the semiconductor laser for supplying a modulating electric current and a bias electric current thereto;

modulating electric current supplying means for supplying said modulating electric current on the basis of an inputted modulating signal; and

bias electric current supplying means for supplying said bias electric current,

wherein first temperature correcting means for increasing said modulating electric current with a rise in ambient temperature is arranged in said modulating electric current supplying means,

a second temperature correcting circuit for increasing said bias electric current with said rise in ambient temperature is arranged in said bias electric current supplying means,

a feedback circuit negatively fed back by said bias electric current is arranged in said bias electric current supplying means,

said first temperature correcting means is constructed by a first resistor circuit constructed by a first thermistor, a first resistor connected in series to said first thermistor, and a second resistor connected in parallel to said first thermistor, and is also constructed by a third resistor connected in series to said first resistor circuit, and

said second temperature correcting means is constructed by a second resistor circuit constructed by a second thermistor, a fourth resistor connected in series to said second thermistor, and a fifth resistor connected in parallel to said second thermistor, and is also constructed by a sixth resistor connected in series to said second resistor circuit, and

said modulating signal is inputted between both terminals of said first temperature correcting means, and

a direct current voltage is applied between both terminals of said second temperature correcting means, and

said modulating electric current is supplied by ~~a~~the modulating signal outputted between both ends of said third resistor, and

said bias electric current is supplied by a divided voltage outputted between both ends of said sixth resistor.

4. (Currently Amended) ~~The~~An optical output control circuit of ~~the~~a semiconductor laser ~~according to claim 2, comprising:~~

the semiconductor laser for supplying a modulating electric current and a bias electric current thereto;

modulating electric current supplying means for supplying said modulating electric current on the basis of an inputted modulating signal; and

bias electric current supplying means for supplying said bias electric current,

wherein first temperature correcting means for increasing said modulating electric current with a rise in ambient temperature is arranged in said modulating electric current supplying means,

a second temperature correcting circuit for increasing said bias electric current with said rise in ambient temperature is arranged in said bias electric current supplying means,

a feedback circuit negatively fed back by said bias electric current is arranged in said bias electric current supplying means,

said feedback circuit is constructed by a transistor for flowing said bias electric current to said semiconductor laser, a seventh resistor connected between an emitter of said transistor and ground, and an operational amplifier interposed between said second temperature correcting means and a base of said transistor, and a divided voltage is applied to a non-inversion input terminal of said operational amplifier, and the emitter of said transistor is connected to an inversion input terminal of said operational amplifier.

5. (New) The optical output control circuit of the semiconductor laser according to claim 4, wherein:

said first temperature correcting means is constructed by a first resistor circuit constructed by a first thermistor, a first resistor connected in series to said first thermistor, and a second resistor connected in parallel to said first thermistor, and is also constructed by a third resistor connected in series to said first resistor circuit,

said second temperature correcting means is constructed by a second resistor circuit constructed by a second thermistor, a fourth resistor connected in series to said second thermistor, and a fifth resistor connected in parallel to said second thermistor, and is also constructed by a sixth resistor connected in series to said second resistor circuit,

said modulating signal is inputted between both terminals of said first temperature correcting means,

a direct current voltage is applied between both terminals of said second temperature correcting means,

said modulating electric current is supplied by the modulating signal outputted between both ends of said third resistor, and

said bias electric current is supplied by a divided voltage outputted between both ends of said sixth resistor.

6. (New) An output control circuit comprising:

a modulating electric current supplier configured to receive a modulating signal and supply a modulating electric current, the modulating electric current supplier containing a first temperature corrector that increases the modulating electric current as an ambient temperature rises; and

a bias electric current supplier that supplies a bias electric current, the bias electric current supplier containing a second temperature corrector that increases the bias electric current as the ambient temperature rises,

wherein the first temperature corrector has a first resistor circuit and a first resistor connected in series to the first resistor circuit, the first resistor circuit includes a first thermistor, a second resistor connected in series to the first thermistor, and a third resistor connected in parallel to the first thermistor, the modulating signal is supplied to an input between terminals of the first temperature corrector, the modulating electric current is supplied by a modulating signal provided to an output between ends of the first resistor, and

the second temperature corrector has a second resistor circuit and a fourth resistor connected in series to the second resistor circuit, the second resistor circuit includes a second thermistor, a fifth resistor connected in series to the second thermistor, and a sixth resistor connected in parallel to the second thermistor, a direct current voltage is applied between terminals of the second temperature corrector, the bias electric current is supplied by a divided voltage provided to an output between ends of the fourth resistor.

7. (New) An output control circuit comprising:

a modulating electric current supplier configured to receive a modulating signal and supply a modulating electric current, the modulating electric current supplier containing a first temperature corrector that increases the modulating electric current as an ambient temperature rises; and

a bias electric current supplier that supplies a bias electric current, the bias electric current supplier containing a second temperature corrector that increases the bias electric current as the ambient temperature rises and a feedback circuit negatively fed back by the bias electric current,

wherein the feedback circuit contains a transistor that supplies the bias electric current, a first resistor connected between an output terminal of the transistor and ground, and an operational amplifier interposed between the second temperature corrector and a control terminal of the transistor, a divided voltage is applied to a non-inversion input terminal of the operational amplifier, and the emitter of the transistor is connected to an inversion input terminal of the operational amplifier.

8. (New) The output control circuit of Claim 7, wherein:

the first temperature corrector has a first resistor circuit and a second resistor connected in series to the first resistor circuit, the first resistor circuit includes a first thermistor, a third resistor connected in series to the first thermistor, and a fourth resistor connected in parallel to the first thermistor, the modulating signal is supplied to an input between terminals of the first temperature corrector, the modulating electric current is supplied by a modulating signal provided to an output between ends of the second resistor, and

the second temperature corrector has a second resistor circuit and a fifth resistor connected in series to the third resistor circuit, the second resistor circuit includes a second thermistor, a sixth resistor connected in series to the second thermistor, and a seventh resistor connected in parallel to the second thermistor, a direct current voltage is applied between terminals of the second temperature corrector, the bias electric current is supplied by a divided voltage provided to an output between ends of the fifth resistor.